

PCT

WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁷ : G02B 6/13	A1	(11) International Publication Number: WO 00/57222 (43) International Publication Date: 28 September 2000 (28.09.00)
(21) International Application Number: PCT/AU00/00219 (22) International Filing Date: 20 March 2000 (20.03.00) (30) Priority Data: PP 9307 18 March 1999 (18.03.99) AU (71) Applicant (for all designated States except US): THE UNIVERSITY OF SYDNEY [AU/AU]; Parramatta Road, Sydney, NSW 2006 (AU). (72) Inventors; and (75) Inventors/Applicants (for US only): BAZYLENKO, Michael [AU/AU]; 15A Deakin Street, Forestville, NSW 2087 (AU). SCEATS, Mark [AU/AU]; Australian Photonics Pty Ltd, 101 National Innovation Centre, Australian Technology Park, Eveleigh, NSW 1403 (AU). (74) Agent: GRIFFITH HACK; G.P.O. Box 4164, Sydney, NSW 2001 (AU).	(81) Designated States: AU, CA, JP, KR, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published With international search report.	

(54) Title: WAVEGUIDE STRUCTURE AND METHOD OF FORMING THE WAVEGUIDE STRUCTURE

(57) Abstract

A method for forming a high optical confinement waveguide structure comprising the steps of: forming a silicon-based waveguide on a substrate by depositing a waveguide layer of silicon containing material onto the substrate; wherein the material is selected in a manner such that the refractive index of the waveguide is greater than the refractive index of the substrate; wherein the forming of the silicon-based waveguide further comprises etching the deposited waveguide structure such as to form a ridge structure in the deposited waveguide layer; wherein the method further comprises the step of forming an optical signal processing element in and integrated with the deposited waveguide layer.

